

FEATURES

- Logic translation
- Bi-Directional
- Invert / non-invert
- TTL-CMOS conversion
- Toroidal construction
- Low profile
- 500Vrms isolation
- Backward compatible with Sn/Pb soldering systems

DESCRIPTION

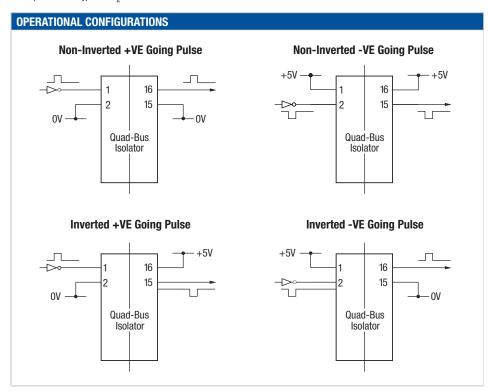
The 1600 series are quad isolation transformer packs for use in digital circuits. They are particularly suited to isolating microprocessor bus connections and performing logic level translation. Being completely passive, power savings are offered over opto-coupled or emitter follower solutions.

SELECTION GUIDE								
Order Code	Туре	Function	Turns Ratio	Max. Pulse Width	Typ. Primary Inductance	Typ. Inerwinding Capacitance		
				μs	mH	pF		
1600C	Quad isolator	5V to 5V logic isolation	1:1	5	3	60		
1630C	Quad isolator	5V to 15V logic isolation	1:3	2.6	1	34		

ABSOLUTE MAXIMUM RATINGS						
Operating free air temperature range	0°C to 70°C					
Storage temperature range	-40°C to 125°C					
Isolation voltage (flash tested for 1 second)	500Vrms					
Insulation resistance at 500Vpc	>10GΩ					

SOLDERING INFORMATION ²					
Peak wave solder temperature, 1.5mm from case	300°C for 10 seconds				
Pin finish	Matte tin				

- 1 The maximum pulse width limit to be applied to the repetition curve.
- 2 For further information, please visit www.murata-ps.com/rohs All specifications typical at $T_{\rm A}$ =25°C.









Quad Data-bus Isolators

PACKAGE SPECIFICATIONS MECHANICAL DIMENSIONS PIN CONNECTIONS 22.50 (0.886) 16 15 14 13 12 11 10 6.86 **1600C** N N N N (0.270)•··· •··· **XYYWW Murata Power Solutions** $\overline{\sim}$ $\overline{\mathbf{V}}$ 7 ∇ 1 1 1 1 5.80 1 2 3 4 5 6 7 8 (0.228)4.50 (0.177) 0.64±0.15 (0.025±0.006) 2.54 (0.10) 0.50±0.05 17.78 (0.7) (0.02 ± 0.002) All dimensions in mm 0.26±0.05 (inches) \pm 0.25 (0.001) (0.01 ± 0.002) unless otherwise stated. Package weight: 1.7g Typ. Package quantity: 20 -8.85 (0.348)-